

VERSION WITH MARKINGS TO SHOW CHANGES MADE

Please amend the title as follows:

[UNIVERSAL SERIAL BUS (USB) GOLDEN PRODUCTION TEST MODE]
APPARATUS AND METHOD TO TEST HIGH SPEED DEVICES WITH A LOW SPEED
TESTER

Please add the following paragraph beginning at page 1,
line 1:

Cross Reference to Related Applications

The present application may relate to co-pending
application Serial No. 09/658,894 filed September 11, 2000.

Please replace the paragraph starting on page 12, line 9
with the following:

The transmit test block 208 generally comprises a place
in transmit mode state 216 (for the device under test portion 202)
and a place in receive test mode state 218, a decision block 220
and a decision block 222 (for the tester portion 204). The place
in transmit test mode state 216 may place a DUT in a transmit test
mode. The place in receive test mode state 218 may place a tester
device in a receive test mode. The place in transit mode state 216

and the place in receive mode state 218 may allow a tester device to correctly test a transmit operation of the DUT. The tester portion (e.g., the golden part 102) 204 may control the DUT portion (e.g., the DUT 104) 202 during the transmit test block 208. Additionally, the DUT [102] portion 202 and/or the tester [104] portion 204 may be controlled by another appropriate device.

Please replace the paragraph starting on page 14, line 7 with the following:

The receive test section 210 generally comprises a place in receive test mode state 226, a decision state 228 and a decision state 230 (for the device under test section 202) and a place in transmit test mode state 232 (for the tester section 204). The tester 204 may be implemented to control the DUT 202 during the receive test block 210. However, the DUT 202 and/or the tester 204 may be controlled by another appropriate type device. The state 226 may place the DUT in a receive test mode. The decision block 228 may check if a "DONE indication" has been received. The DONE indication may indicate if a test packet has been correctly received by the DUT. The DONE indication may be implemented internal to the DUT 202. However, the DONE indication may be generated by another appropriate type device in order to meet the criteria of a particular implementation. If a DONE indication has

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been received, the decision block 228 may enter a test passed state 234. If a DONE indication is not received, the decision block 228 may enter the decision block 230. If the decision block 230 determines that a "DONE timeout" is to occur, the decision block [228] 230 may enter the test failed block 224. If the decision block 230 determines that a DONE timeout is not to occur, the decision block 230 may move to the decision block 228.

Please replace the paragraph starting on page 16, line 7 with the following:

The conventional low speed tester 302 may have an output 312 that may present a signal (e.g., PASS/FAIL), an output 314 that may present a transmission signal (e.g., TA), an input 316 that may receive a reception signal (e.g., RE) and an input 318 that may receive a signal (e.g., TV). The signal PASS/FAIL may indicate a pass/fail condition of a DUT 310. The signal PASS/FAIL may be asserted and/or deasserted to indicate a particular condition of the DUT 310. The test vectors section 308 may generate the signal TV. In one example, the signal TV may be implemented as testing vectors. However, the signal TV may be implemented as another appropriate type signal in order to meet the criteria of a particular implementation. The tester vectors 308 may provide

testing vectors TV to the conventional low speed tester 302 in order to test the DUT 310.

Please replace the abstract with the following:

An apparatus coupled to a low speed tester and a device is disclosed. The device may have a first speed faster than a second speed of the low speed tester. The apparatus may be configured to allow the low speed tester to perform high speed tests of the device at the first speed.